

<b>Notice of References Cited</b>		Application/Control No. 09/826,652	Applicant(s)/Patent Under Reexamination OLSEN ET AL.	
		Examiner Ted M Wang	Art Unit 2634	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,631,925	05-1997	Koenzen, Ralf	375/225
	B	US-5,982,837	11-1999	Earnest, Tim	375/377
	C	US-6,163,808	12-2000	Kilkki, Matti Kalevi	709/233
	D	US-6,215,414	04-2001	Kuramatsu, Hiroyasu	340/7.2
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	J. Christoph Scheytt, "a 0.155-. 0.622-, and 2.488-Gb/s Automatic Bit-Rate Selecting Clock and Data Recovery IC for Bit-Rate Transparent SDH System", IEEE Journal of Solid-state Circuits, Vol. 34, No. 12. pp. 1935-1943, December 1999
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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